

AMENDMENTS TO THE CLAIMS

1. (Original) An AT-command analyzing device, comprising:
 - a transmitting and receiving portion which receives asynchronous transmission serial data based on a baud-rate clock from a DTE;
 - a control portion which analyzes the data received by said transmitting and receiving portion;
 - a baud-rate generating portion which generates the baud-rate clock to be output to said transmitting and receiving portion in accordance with instructions from said control portion;
 - a measuring portion which measures the span of the start bit of the first character of an AT command transmitted from said DTE based on instructions from said control portion;
 - a rate analyzing portion which receives a measurement result of said measuring portion, outputs frequency-dividing data for producing a clock for sampling the first character, and also outputs, when the rate of the start bit is more than a preset value, a flag indicating this matter;
 - a sampling-clock generating portion which selects, in accordance with whether or not said flag is has been set, the frequency-dividing data from either said rate analyzing portion or said control portion, and produces the sampling clock; and
 - a shift-register portion which receives data subsequent to the start bit of the first character based on the sampling clock from said sampling-clock generating portion, and holds the received data, which data is then read by said control portion.

Application No.: Not Yet Assigned

Docket No.: R2184.0066/P066-A

2-5. (Canceled)